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Sunder

Attorney Docket No.: 60188-084  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :  
Shigeki FURUYA, et al. : Group Art Unit: 2815  
Serial No.: 09/922,671 : Examiner: Matthew E. WARREN  
Filed: August 07, 2001 :  
For: CMOS BASIC CELL AND METHOD FOR FABRICATING SEMICONDUCTOR  
INTEGRATED CIRCUIT USING THE SAME

PRELIMINARY AMENDMENT

Hon. Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

Prior to examination of the above-identified application please amend the  
application as follows:

IN THE CLAIMS:

Please amend claims 22, 23, 26 and 27 and insert new claim 41 as follows:

22. (Amended) A CMOS basic cell comprising:  
an N-channel transistor, a P-channel transistor and an interconnect connected to  
said N-channel or P-channel transistor through a contact hole on a semiconductor  
substrate; and

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